

# University of Pretoria Yearbook 2020

## DSP programming 300 (ESP 300)

<b>Qualification</b>	Undergraduate
<b>Faculty</b>	Faculty of Engineering, Built Environment and Information Technology
<b>Module credits</b>	4.00
<b>Programmes</b>	BEng Electrical Engineering BEng Electrical Engineering ENGAGE
<b>Prerequisites</b>	EPW 200, (ELI 220), (EMK 310)
<b>Contact time</b>	Three days
<b>Language of tuition</b>	Module is presented in English
<b>Department</b>	Electrical, Electronic and Computer Engineering
<b>Period of presentation</b>	Year

### Module content

This module is presented during one of the recess periods in the third year of study. The module is an introduction to digital signal processors (DSPs) for electrical engineering students. The first of three days is dedicated to theory lectures introducing DSP and addressing quantisation, sampling theory, anti-aliasing filters, correlation, convolution, DFT, inverse DFT, Z- transforms, digital filters (low pass, anti-aliasing, FIR and IIR) and the design thereof. PCB layout techniques, decoupling and bypass capacitors relating to digital circuits are addressed. At the end of the theory sessions students need to design a filter. The practical work over the last two days consists of implementing the filter designed as well as coding DAC, FIR, IIR and PWM for a DSP. The DAC, FIR, IIR and PWM are implemented in hardware/firmware and the results displayed on an oscilloscope. At the end of the module each student will demonstrate a working system consisting of the developed firmware and hardware performing the required signal processing functions. Students will be informed by the Department if, for practical reasons, the module needs to be offered in a different time slot.

The information published here is subject to change and may be amended after the publication of this information. The [General Regulations \(G Regulations\)](#) apply to all faculties of the University of Pretoria. It is expected of students to familiarise themselves well with these regulations as well as with the information contained in the [General Rules](#) section. Ignorance concerning these regulations and rules will not be accepted as an excuse for any transgression.